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Confirmation No. 6633IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Theodore Letavic	Examiner:	Kebede, Brook
Serial No.:	10/560,573	Group Art Unit:	2823
Filed:	December 12, 2005	Docket No.:	US030162US2
Title:	High-Side CMOS in an Integrated SOI Process		

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence and the papers, as described hereinabove, are being transmitted via facsimile-Formal Entry, to the attention of the Examiner at Commissioner for Patents, MAIL STOP AMENDMENT, P.O. Box 1450, Alexandria, VA 22313-1450, On July 26, 2007.

By: 

Kelly J. L. Linn

Facsimile No.: 571 273-8300

OFFICE ACTION RESPONSE AND AMENDMENT

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Customer No.  
65913

Dear Sir:

In response to the non-final Office Action dated April 26, 2007, please reconsider the application in view of the following amendments and remarks.

Amendments to the abstract are found on page 2 of this paper.

A complete listing of the claims, to include any amendments presented therein, begins on page 4 of this paper.

Remarks/Arguments follow on page 5.

Authorization is provided to charge/credit Deposit Account 50-0996 (NXPS.246PA) any requisite fees/overages to enter this paper.